

please Enter it
6
f.l
12/3104

Docket No. 034299-389

AF/2673/8
A



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED

OCT 26 2004

Technology Center 2600

APPLICANT: Pierre Nicolas
SERIAL NO.: 10/076,454
FILING DATE: February 19, 2002
TITLE: FLAT THERMIONIC EMISSION SCREEN AND WITH
INTEGRATED ANODE CONTROL DEVICE
EXAMINER: Lao, Lun Yi
ART UNIT: 2673

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MS Non Fee Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

Date: 10/20/04 Name: Diane Morse
Diane Morse

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION (RCE)

Dear Sir:

In response to the Final Office Action of 07/08/2004, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A flat thermionic emission screen (1) comprising:
 - a first substrate (2) on which are arranged an emission cathode (4) and an electron extraction grid (8),
 - a second substrate (12) facing the first substrate (2), on which is arranged an anode (14) designed to collect the electrons emitted by the cathode(4), and
 - an electronic control circuit (19) of the anode voltage (14) comprising at least one commutation component (18,20), wherein the commutation component (18,20) is ~~integrated through design in the first substrate (2) and in the second substrate (12) of the screen (1) an HV transistor with a first electrode (40, 50) integrated in the first substrate (2), a second electrode (42,52) integrated in the second substrate (12) and a third electrode (44,54) integrated in the extraction grid (8).~~
2. (canceled)
3. (previously presented) The screen according to claim 1, wherein the anode (14) constitutes the emissive surface of the screen (1) and comprises at least one conducting surface (15) on which phosphor materials (17) are deposited.